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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number TBA
Filing Date Herewith
First Named Inventor Christopher B. Koon
Group Art Unit 2815
Examiner Name E. Ortiz (Recommended)
Attorney Docket Number 90065.146801

10/03/01

11/09/01

U.S. PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
10/03/01	*1	4,969,028		Baliga	11/06/1990	
10/03/01	*2	4,364,073		Becke, et al.	12/14/1982	
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Examiner Initials ¹	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
10/03/01	*13	JP	02144971		Hitachi, Ltd.	04/06/1990		
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10/03/01	*17	JP	405-82791		Maruyama	04/1993		
10/03/01	*18	JP	405-347414		Terajima	10/1993		

Examiner Signature

Date Considered

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Substitute for form 1449B/PTO		C m p l t e i f K n w n	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (us as many sheets as necessary)		Application Number	TBA
		Filing Date	Herewith
		First Named Inventor	Christopher B. Kocon
		Group Art Unit	2815
		Examiner Name	E. Ortiz (Recommended)
Sheet 2	of 2	Attorney Docket Number	90065.146801

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	19	Jonathan Evans, et al., "The Behaviour of Very High Current Density Power MOSFET's", <u>IEEE Electron Devices Society</u> , pp. 157-160 (May 1996).	
		Jonathan Evans, et al., "The Behavior of Very High Current Density Power MOSFET's", <u>IEEE Transactions on Electron Devices</u> , Vol. 44. No. 7, pp. 1148-1153 (July 1997).	
	21	Bulucea, Constantin, et al., "Trench DMOS Transistor Technology for High-Current (100 A Range) Switching", <u>Solid-State Electronics</u> , Vol. 34, Vol. 5, Head Hill Hall, Oxford, GB, pp. 493-507 (May 1991).	
		Matsumoto, Satoshi, et al., "A High Performance Self-Aligned UMOSFET With a Vertical Trench Contact Structure", <u>IEEE Transactions on Electron Devices</u> , Vol. 41, No. 5, New York, New York, US, pp. 1-5 (May 1994).	
	23	International Search Report	

Examiner Signature	Date Considered
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